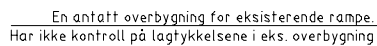


Diagram illustrating a 4-stage pipeline (CL) with stages: Stages, Branch, Instruction, and Write Back. Each stage has a delay of 0.5. The total delay is 1.5. The diagram shows the flow of data through the stages and the resulting output.



- Bentonittmembran
- Duk fremmedarter
- Antatt eksisterende vegoverbygning
- Tettepropp
- Bekkelukning
- Ny terrengoverflate
- Eksisterende terreng
- Avlastning
- Grusvei
- Vegetasjonslag

F03